

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE


[Membership](#) [Publications/Services](#) [Standards](#) [Conferences](#) [Careers/Jobs](#)
IEEE Xplore®
 RELEASE 1.8

 Welcome
 United States Patent and Trademark Office


>> See

[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)
[Quick Links](#)

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced
- ☐ CrossRef

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

IEEE Enterprise

- ☐ Access the IEEE Enterprise File Cabinet

Print Format

Your search matched **11** of **1091947** documents.A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.**Refine This Search:**

You may refine your search by editing the current search expression or entering a new one in the text box.

☐ Check to search within this result set
Results Key:**JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard**1 A second-order double-sampled delta-sigma modulator using individual-level averaging***Thanh, C.K.; Lewis, S.H.; Hurst, P.J.;*

Solid-State Circuits, IEEE Journal of , Volume: 32 , Issue: 8 , Aug. 1997

Pages:1269 - 1273

[\[Abstract\]](#) [\[PDF Full-Text \(172 KB\)\]](#) **IEEE JNL**
2 Granular quantization noise in the first-order delta-sigma modulator*Galton, I.;*

Information Theory, IEEE Transactions on , Volume: 39 , Issue: 6 , Nov. 1993

Pages:1944 - 1956

[\[Abstract\]](#) [\[PDF Full-Text \(968 KB\)\]](#) **IEEE JNL**
3 A 2nd-order double-sampled $\Delta\Sigma$ modulator with individual-level averaging*Thanh, C.; Lewis, S.H.; Hurst, P.J.;*

VLSI Circuits, 1996. Digest of Technical Papers., 1996 Symposium on , 13-15 1996

Pages:100 - 101

[\[Abstract\]](#) [\[PDF Full-Text \(236 KB\)\]](#) **IEEE CNF**
4 Design techniques for CMOS micropower low-voltage switched-capacitor delta-sigma modulator*Tsung-Sum Lee; Wen-Bin Lin; Dung-Lin Lee;*

Advanced System Integrated Circuits 2004. Proceedings of 2004 IEEE Asia-Pacific Conference on , 4-5 Aug. 2004

Pages:28 - 31

[\[Abstract\]](#) [\[PDF Full-Text \(357 KB\)\]](#) IEEE CNF

5 A novel continuous-time common-mode feedback for low-voltage switched-OPAMP

Ali-Bakhshian, M.; Sadeghi, K.;

Low Power Electronics and Design, 2004. ISLPED '04. Proceedings of the 2004 International Symposium on , 9-11 Aug. 2004
Pages:296 - 300

[\[Abstract\]](#) [\[PDF Full-Text \(393 KB\)\]](#) IEEE CNF

6 IF-sampling fourth-order bandpass /spl Delta//spl Sigma/ modulator for digital receiver applications

Cosand, A.E.; Jensen, J.F.; Choe, H.C.; Fields, C.H.;

Solid-State Circuits, IEEE Journal of , Volume: 39 , Issue: 10 , Oct. 2004
Pages:1633 - 1639

[\[Abstract\]](#) [\[PDF Full-Text \(816 KB\)\]](#) IEEE JNL

7 Rigorous analysis of delta-sigma modulators for fractional-N PLL frequency synthesis

Kozak, M.; Kale, I.;

Circuits and Systems I: Regular Papers, IEEE Transactions on [see also Circuit Systems I: Fundamental Theory and Applications, IEEE Transactions on] , Vol 51 , Issue: 6 , June 2004
Pages:1148 - 1162

[\[Abstract\]](#) [\[PDF Full-Text \(872 KB\)\]](#) IEEE JNL

8 A 90-dB SNR 2.5-MHz output-rate ADC using cascaded multibit delta sigma modulation at 8x oversampling ratio

Fujimori, I.; Longo, L.; Hairapetian, A.; Seiyama, K.; Kosic, S.; Jun Cao; Shu-Chan;

Solid-State Circuits, IEEE Journal of , Volume: 35 , Issue: 12 , Dec. 2000
Pages:1820 - 1828

[\[Abstract\]](#) [\[PDF Full-Text \(184 KB\)\]](#) IEEE JNL

9 Design and simulation of Fractional-N PLL frequency synthesizers

Kozak, M.; Friedman, E.G.;

Circuits and Systems, 2004. ISCAS '04. Proceedings of the 2004 International Symposium on , Volume: 4 , 23-26 May 2004
Pages:IV - 780-3 Vol.4

[\[Abstract\]](#) [\[PDF Full-Text \(285 KB\)\]](#) IEEE CNF

10 IF sampling 4th order bandpass /spl Delta//spl Sigma/ modulator for digital receiver applications

Jensen, J.F.; Cosand, A.E.; Choe, H.C.;

Gallium Arsenide Integrated Circuit (GaAs IC) Symposium, 2003. 25th Annual Technical Digest 2003. IEEE , 9-12 Nov. 2003
Pages:200 - 203

[\[Abstract\]](#) [\[PDF Full-Text \(377 KB\)\]](#) IEEE CNF

11 **Delta-sigma modulation applications in neutral-point clamped inver**
Dai, S.; von Jouanne, A.; Wallace, A.; Temes, G.C.;
Industry Applications Conference, 2003. 38th IAS Annual Meeting. Conference
Record of the , Volume: 1 , 12-16 Oct. 2003
Pages:678 - 683 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(426 KB\)\]](#) IEEE CNF

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) |
[New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online](#)
[Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved

